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Sheet	1	of	1	Application Number	10/632,362
				Filing Date	July 31, 2003
				First Named Inventor	Robert F. ENENKEL et al
				Group Art Unit	2123
				Examiner Name	Unassigned
				Attorney Docket Number	YOR920030200U!

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DHM		ABRAMOWITZ & STEGUN, "Handbook of Mathematical Functions", 1964, pp. 14, 15, 18.	
DHM		"IBM PowerPC 440 FPU Brings Supercomputing Into the Embedded Space", IBM PowerPC Processor News, August 2001. http://www-3.ibm.com/chips/products/powerpc/newsletter/aug2001	
DHM		IEEE 754, IEEE Standard for Binary Floating-Point Arithmetic, ANSI/IEEE Std 754-1985, IEEE Standard for Binary Floating-Point Arithmetic, IEEE, New York, 1985.	

Examiner Signature	/David Malzahn/	Date Considered	09/19/2006
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